

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"6496971".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L2	55	decrypt\$4 keys same (configuration or boot) same (PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L3	77	(configurat\$4 or boot) near "3" register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L4	77981	(PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L5	13	L3 and L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L6	234	configuration data register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L7	53	L4 and L6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47

L8	24	L4 and (microcontroller or microprocessor) near5 data register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L9	4786	713/1.ccls. or 713/2.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L10	365	L4 and L9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L11	4235	713/189.ccls. or 713/190.ccls. or 713/191.ccls. or 713/193.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L12	274	L4 and L11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L13	1381	380/44.ccls. or 380/45.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L14	10208	L9 or L11 or L13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L15	564	decrypt\$4 keys and (configuration or boot) and (PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47

L16	88	L14 and L15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L17	3	minimum with maximum same ROM same program counter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L18	5	10/714380	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L19	8	boot same (FPGA or PLD) same configuration same encrypt\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L20	242	boot same (FPGA or PLD) same configuration	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L21	31	(PLD or FPGA) same (microcontroller or microprocessor) same key same (configuration or initialization or boot)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L22	2	"6496971".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L23	92	(configuration or initialization or boot) same (register or memory) same (prevent\$4 or deni\$4 or cannot) same access \$4 same (FPGA or PLD)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47

L24	16	(key storage) same (FPGA or PLD) same (encrypt\$4 or decrypt \$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L25	40	(key (storage or memory)) same (FPGA or PLD) same (encrypt \$4 or decrypt\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L26	909	716/16.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L27	564	decrypt\$4 keys and (configuration or boot) and (PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L28	242	boot same (FPGA or PLD) same configuration	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L29	27	(L27 or L28) and L26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L30	2	"6496971".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L31	55	decrypt\$4 keys same (configuration or boot) same (PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47

L32	77	(configurat\$4 or boot) near "3" register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L33	77981	(PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L34	13	L32 and L33	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L35	234	configuration data register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L36	53	L33 and L35	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L37	24	L33 and (microcontroller or microprocessor) near5 data register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L38	4786	713/1.ccls. or 713/2. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L39	365	L33 and L38	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47

L40	4235	713/189.ccls. or 713/190.ccls. or 713/191.ccls. or 713/193.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L41	274	L33 and L40	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L42	1381	380/44.ccls. or 380/45. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L43	10208	L38 or L40 or L42	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L44	564	decrypt\$4 keys and (configuration or boot) and (PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L45	88	L43 and L27	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L46	3	minimum with maximum same ROM same program counter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L47	5	10/714380	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47

L48	8	boot same (FPGA or PLD) same configuration same encrypt\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L49	242	boot same (FPGA or PLD) same configuration	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L50	31	(PLD or FPGA) same (microcontroller or microprocessor) same key same (configuration or initialization or boot)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L51	2	"6496971".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L52	92	(configuration or initialization or boot) same (register or memory) same (prevent\$4 or deny\$4 or cannot) same access \$4 same (FPGA or PLD)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L53	16	(key storage) same (FPGA or PLD) same (encrypt\$4 or decrypt \$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L54	40	(key (storage or memory)) same (FPGA or PLD) same (encrypt \$4 or decrypt\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 11:47
L55	5	10/714380	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/09/09 14:26

L56	71	("20010015919" "20010032318" "20010037458" "20020018561" "20020059518" "20020064282" "20020069257" "20030091185" "3849760" "4037214" "4757468" "5084636" "5091938" "5121359" "5237218" "5237219" "5293424" "5321704" "5327564" "5341034" "5343406" "5349249" "5375169" "5388157" "5394031" "5442704" "5457408" "5594793" "5598424" "5748740" "5768372" "5774544" "5838901" "5914616" "5930826" "5933023" "6028445" "6049222" "6058477" "6118869" "6172520" "6212639" "6237124" "6259791" "6305005" "6324676" "6330668" "6331784" "6356637" "6366117" "6441641" "6442092" "6473861" "6477677" "6598161" "6636971" "6662333" "6785816" "6823069" "6883075" "6904527" "6931128" "6931543" "6948026" "6957340" "RE34363" "RE37195").PN. OR ("7129237").URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2008/09/09 14:33
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S1	2	"6496971".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 12:47
S2	33	decrypt\$4 keys same (configuration or boot) same (PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:08
S3	67	(configurat\$4 or boot) near "3" register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 12:48
S4	54741	(PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 12:49
S5	12	S3 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 12:52
S6	186	configuration data register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 12:52
S7	36	S4 and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 12:59
S8	22	S4 and (microcontoller or microprocessor) near\$5 data register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:00

S9	3787	713/1.ccls. or 713/2.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:03
S10	261	S4 and S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:04
S11	3035	713/189.ccls. or 713/190.ccls. or 713/191.ccls. or 713/193.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:05
S12	174	S4 and S11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:05
S13	1042	380/44.ccls. or 380/45.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:08
S14	7743	S9 or S11 or S13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:08
S15	367	decrypt\$4 keys and (configuration or boot) and (PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:09
S16	59	S14 and S15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 13:55

S17	3	minimum with maximum same ROM same program counter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/03/15 14:12
S18	2	10/714380	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/25 16:30
S19	7	boot same (FPGA or PLD) same configuration same encrypt\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/27 09:52
S20	177	boot same (FPGA or PLD) same configuration	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/08/27 09:52
S21	31	(PLD or FPGA) same (microcontroller or microprocessor) same key same (configuration or initialization or boot)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 12:42
S22	2	"6496971".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 12:46
S23	80	(configuration or initialization or boot) same (register or memory) same (prevent\$4 or deny\$4 or cannot) same access \$4 same (FPGA or PLD)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 13:22
S24	15	(key storage) same (FPGA or PLD) same (encrypt\$4 or decrypt \$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 14:09

S25	36	(key (storage or memory)) same (FPGA or PLD) same (encrypt \$4 or decrypt\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 14:09
S26	819	716/16.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:58
S27	497	decrypt\$4 keys and (configuration or boot) and (PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S28	201	boot same (FPGA or PLD) same configuration	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S29	27	(S27 or S28) and S26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S30	2	"6496971".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S31	46	decrypt\$4 keys same (configuration or boot) same (PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S32	73	(configurat\$4 or boot) near "3" register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59

S33	69734	(PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S34	12	S32 and S33	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S35	218	configuration data register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S36	45	S33 and S35	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S37	23	S33 and (microcontroller or microprocessor) near5 data register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S38	4403	713/1.ccls. or 713/2.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S39	334	S33 and S38	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S40	3851	713/189.ccls. or 713/190.ccls. or 713/191.ccls. or 713/193.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59

S41	241	S33 and S40	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S42	1224	380/44.ccls. or 380/45. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S43	9311	S38 or S40 or S42	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S44	497	decrypt\$4 keys and (configuration or boot) and (PLD or programmable logic device or FPGA or field programmable logic array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S45	77	S43 and S27	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S46	3	minimum with maximum same ROM same program counter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S47	3	10/714380	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S48	8	boot same (FPGA or PLD) same configuration same encrypt\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59

S49	201	boot same (FPGA or PLD) same configuration	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S50	31	(PLD or FPGA) same (microcontroller or microprocessor) same key same (configuration or initialization or boot)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S51	2	"6496971".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S52	80	(configuration or initialization or boot) same (register or memory) same (prevent\$4 or deny\$4 or cannot) same access \$4 same (FPGA or PLD)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S53	15	(key storage) same (FPGA or PLD) same (encrypt\$4 or decrypt \$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59
S54	36	(key (storage or memory)) same (FPGA or PLD) same (encrypt \$4 or decrypt\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2008/03/14 17:59

9/ 9/ 2008 4:26:28 PM

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